

# EXHIBIT 7

## (PART 4 OF 4)

## SECTION 19

### COMPUTER INTERFACE

#### Computer Interface

The standard interface supplied with the 5216 Standard Firmware is compatible with the user inputs to a DEC DR-11B DMA controller. A summary of the interface signals between the DR-11B and the 5216 is shown in table 19-1. All transfers of instructions and data are performed in the DMA mode. Transfers are set up by first loading the word count and bus address registers of the DR-11B, thus specifying the length of the transfer and the starting location in the computer system memory from/to which data is to be transferred. The type of operation (input or output) is then specified by loading the Device Function bits of the CSR of the DR-11B.

DR11-B, Function #1 defines the direction of data transfer.

<u>F1</u>	<u>OPERATION</u>
0	Transfer from Computer to DG
1	Transfer from DG to Computer

Function bit F3 is used to reset Attention and the Device Status Bits (DSTAT, A, B, C). F3 may also be used to mask interrupts.

#### Interrupts

An interrupt to the DR-11B is generated when:

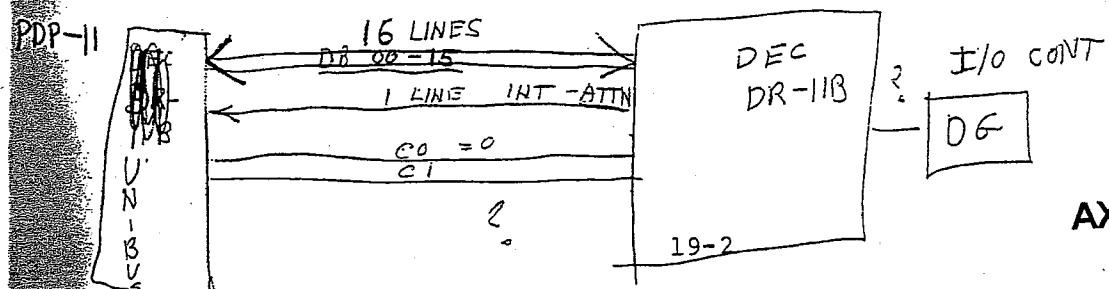
- a. A keystroke is to be transmitted to the computer.
- b. An internal error is detected.
- c. A transfer of data from the Display Generator is completed.

The status bits from the DG to the DR-11B indicate which of the above conditions generated the interrupt. If an operator initiates an operation from a keyboard during an I/O transfer, the DG will wait until the I/O transfer is completed before generating the keyboard interrupt.

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Table 19-1. Interface Signal Summary  
 Model 5216 to DEC DR-11B  
 DMA Controller

Name	Mnemonic	Function
Output Data Bits 00-15 (16 lines)	DAT00 OUT through DAT15 OUT	Data from UNIBUS to display generator (via DR-11B)
Input Data Bits 00-15 (16 lines)	DAT00 IN through DAT 15 IN	Data from display generator to UNIBUS (via DR-11B)
Interrupts (1 line)	ATTN	Interrupt command to the PDP-11 processor
I/O Control (2 lines)	C0, C1	C0 = 0 always C1 = 0: from UNIBUS to Display C1 = 1: from Display to UNIBUS
Output Data Ready or In- put Data Acknowledge (1 line)	END CYCLE	Output Data Ready from DR-11B if C1 = 0. Input Data Acknowledge from DR-11B if C1 = 1
Input Data Ready or Output Data Acknowledge (1 line)	CYCLE REQUEST A	Input Data Ready to DR-11B if C1 = 1. Output Data Acknowledge to DR-11B if C1 = 0
Device Status A,B,C (3 lines)	DSTAT A,B,C	Status lines to DR-11B (PROM 5216)
Device Function 1,2,3 (3 lines)	FNCT 1,2,3	Control lines from DR-11B to 5216
Ready (1 line)	RDY	Signal to 5216, which when false, indicates that a transfer (DMA) is in progress
Initialize (1 line)	INIT	Not used
Word Count Increment Enable	WC INC ENB	Tied to +5V
Bus Address Increment Enable	BA INC ENB	Tied to +5V
Address 00	A00	Tied to ground
Single Cycle	--	Tied to +5V
Cycle Request B	--	Tied to ground
Busy	BUSY	Signal to 5216, which when true, indicates that a bus cycle (on the UNIBUS) is in progress
Go	GO	Pulse signal to 5216 which causes 5216 to request the first bus cycle of a DMA transfer



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## Device Status Bits (DSTAT A, B, C)

The Device Status Bits produced by the 5216 are defined as follows:

<u>STATUS BIT</u>	<u>FUNCTION</u>
DSTAT A	Keyboard Transmit Request
DSTAT B	End of Transmission (EOT)
DSTAT C	5216 Error
DSTAT A & B	Transmit from ANCS request

## General Device Handler

The software necessary to handle the various transfers to and from the 5216 Display Generator should follow the general structures illustrated in the next section.

Data Transfer From Computer To Display Generator

The sequence of operations for a transfer to the Display Generator is as follows:

- a. Load 2's complement of word count into DR11-B word count register (DRWC).
- b. Load bus address (core location) of first output data word into DR11-B bus address register (DRBA).
- c. Reset Function #1 (write to DG) in the DR11-B Status and Command register (DRST)
- d. Enable the DR11-B interrupt and start the transfer by setting the GO bit in DRST.

**Note**

DO NOT reset Function #1 and set GO in the same instruction.  
DO NOT set CYCLE in DRST.

Data Transfer From Display Generator to Computer

The sequence of operations necessary to read a message from the Display Generator is:

- a. Load 2's complement of word count into DR11-B word count register (DRWC).

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- b. Load bus address of destination (first location where message data is to be put) in DRBA.
- c. Set Function #1 in DRST.
- d. Enable DR11-B interrupt and start transfer by setting GO in DRST.
- e. Upon completion of Read (word count overflow) pulse F3 to reset ATTN and DSTAT B (EOT).

**Note**

DO NOT set Function #1 and set GO in the same instruction.

DO NOT set CYCLE in DRST.

Termination of I/O via DR11-B

Termination of a DMA transfer will occur when the DRWC overflows (all 1's to all 0's). This will cause the READY bit of DRST to be set.

If the DR11-B interrupt was enabled as above, an interrupt to the CPU will occur at the end of the DMA transfer.

Any I/O will also be terminated when a 5216 error or EOT condition occurs. A DMA transfer will not be terminated if a keyboard transmit request occurs. This prevents operator intervention during transfer operations.

Keyboard Arming

In order to arm (enable) the DR11-B for keystrokes:

- a. Set F1, F2, F3 = 1
- b. Set interrupt enable and GO
- c. Wait for keystroke

Upon receipt of an interrupt for a pending keystroke:

- a. Set F1, F3 = 1; F2 = 0
- b. Set DRWC = -7
- c. Set DRBA
- d. Set F3 = 0
- e. Set GO and interrupt enable

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The seven-word keystroke message will be DMA'ed to the buffer pointed to by DRBA, and DRWC will overflow, producing an interrupt.

Then:

Pulse F3 off, on, and off (0, 1, 0)

The DR11-B and 5216 is now ready for further I/O.

**Note**

Anytime ATTN is set, F3 must be pulsed to reset ATTN and the DSTAT's. The condition of the DSTAT's before this pulse indicates the reason for the ATTN.

DSTAT A - There is a pending keystroke request. A seven-word read as described above must be done after pulsing F3.

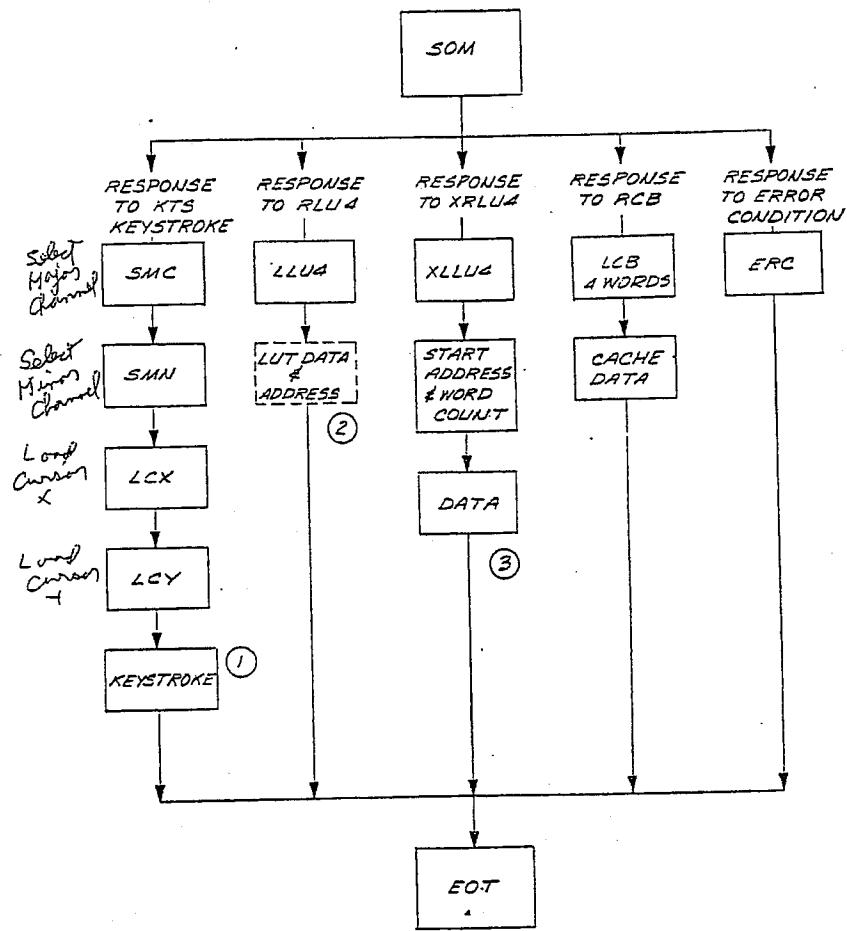
DSTAT B - This indicates an EOT (a readback is complete). Pulse F3 to reset ATTN and DSTAT B.

DSTAT C - This indicates a 5216 error. Pulse F3 and read three words back to get error code.

DSTAT A + B - There is data ready for transmission from the Alphanumeric Channel Set (ANCS).

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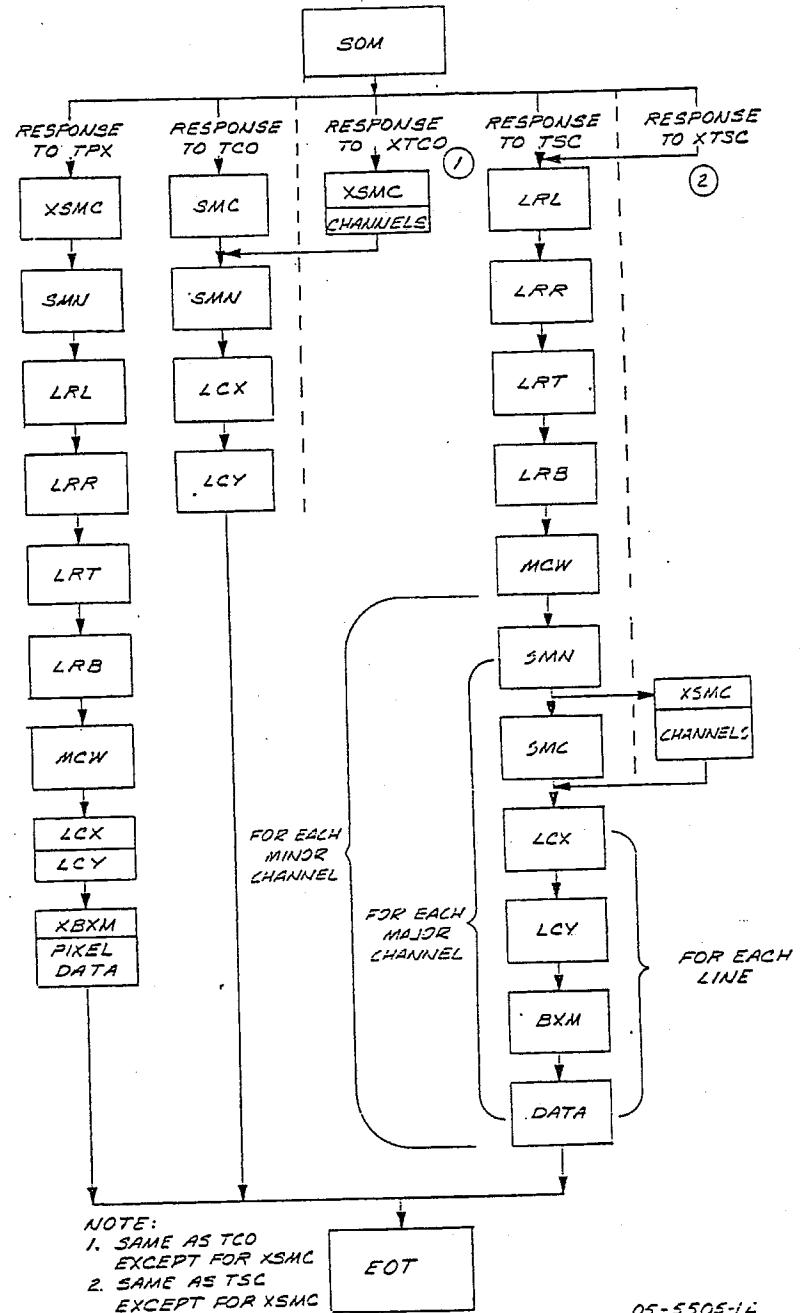
APPENDIX A  
TRANSMIT FORMATS



05-5505-2 A

1. ONE KEYSTROKE PER SEQUENCE - (ASCII CODE LOW BITS ; HIGH 8 BITS 0)
2. COLOR WORD TRANSMITTED IF BIT 7 OF WORD #1 OF RLUA IS SET
3. HIGH 8 BITS 0, LOW EIGHT BITS DATA

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APPENDIX B  
ASCII CHARACTER CODES  
5x7, 10x14 Fonts

Bit	7	6	5	4	3	2	1	X	X	X	X	X
0000	0	0	0	0	0	0	0	0	0	0	0	0
0001	0	0	0	0	0	0	1	1	1	1	1	1
0010	0	0	0	0	0	1	2	2	2	2	2	2
0011	0	0	0	0	0	1	3	3	3	3	3	3
0100	0	0	0	0	0	1	4	4	4	4	4	4
0101	0	0	0	0	0	1	5	5	5	5	5	5
0110	0	0	0	0	0	1	6	6	6	6	6	6
0111	0	0	0	0	0	1	7	7	7	7	7	7
1000	0	0	0	0	0	1	8	8	8	8	8	8
1001	0	0	0	0	0	1	9	9	9	9	9	9
1010	0	0	0	0	0	1	:	:	:	:	:	:
1011	0	0	0	0	0	1	<	<	<	<	<	<
1100	0	0	0	0	0	1	>	>	>	>	>	>
1101	0	0	0	0	0	1	=	=	=	=	=	=
1110	0	0	0	0	0	1	?	?	?	?	?	?
1111	0	0	0	0	0	1	/	/	/	/	/	/

## 7x9 Character Codes

b <sub>7</sub>	0	0	0	0	0	1	1	1	1
b <sub>6</sub>	0	0	0	1	0	0	1	0	1
b <sub>5</sub>	0	1	0	0	1	0	0	1	1
b <sub>4</sub>	0	0	0	0	0	0	0	0	0
b <sub>3</sub>	0	0	0	1	0	0	1	0	1
b <sub>2</sub>	0	0	1	0	0	0	1	0	1
b <sub>1</sub>	0	0	1	1	0	0	1	1	1
b <sub>0</sub>	0	1	0	0	1	0	0	1	1
b <sub>9</sub>	0	1	1	0	0	0	0	0	0
b <sub>8</sub>	0	1	1	1	0	0	0	0	0
b <sub>7</sub>	1	0	0	0	1	0	0	0	0
b <sub>6</sub>	1	0	0	1	0	1	0	0	0
b <sub>5</sub>	1	0	1	0	0	1	0	0	0
b <sub>4</sub>	1	0	1	1	0	0	0	0	0
b <sub>3</sub>	1	0	1	1	0	0	0	0	0
b <sub>2</sub>	1	0	1	1	1	0	0	0	0
b <sub>1</sub>	1	0	1	1	1	0	0	0	0
b <sub>0</sub>	1	1	0	0	1	0	0	0	0
b <sub>9</sub>	1	1	0	1	0	1	0	0	0
b <sub>8</sub>	1	1	0	1	1	0	1	0	0
b <sub>7</sub>	1	1	1	0	0	1	0	0	0
b <sub>6</sub>	1	1	1	0	1	0	1	0	0
b <sub>5</sub>	1	1	1	1	0	0	1	0	0
b <sub>4</sub>	1	1	1	1	1	0	0	0	0
b <sub>3</sub>	1	1	1	1	1	0	0	0	0
b <sub>2</sub>	1	1	1	1	1	0	0	0	0
b <sub>1</sub>	1	1	1	1	1	0	0	0	0
b <sub>0</sub>	1	1	1	1	1	1	0	0	0

## APPENDIX C

## MEMORY ADDRESS

FFFFF

PROCESSOR ROM  
8K WORDS

FC000

OPTIONAL PROCESSOR ROM  
8K WORDS

F8000

FREE SPACE  
464K WORDS

10000

STANDARD SYSTEM MODULES

08000

EA ROM (SHADOWS RAM)

07800

PROCESSOR RAM  
16K WORDS

00400

INTERRUPT VECTORS

00000

ON PROCESSOR BOARD

OFF BOARD  
ADDRESSING SPACE

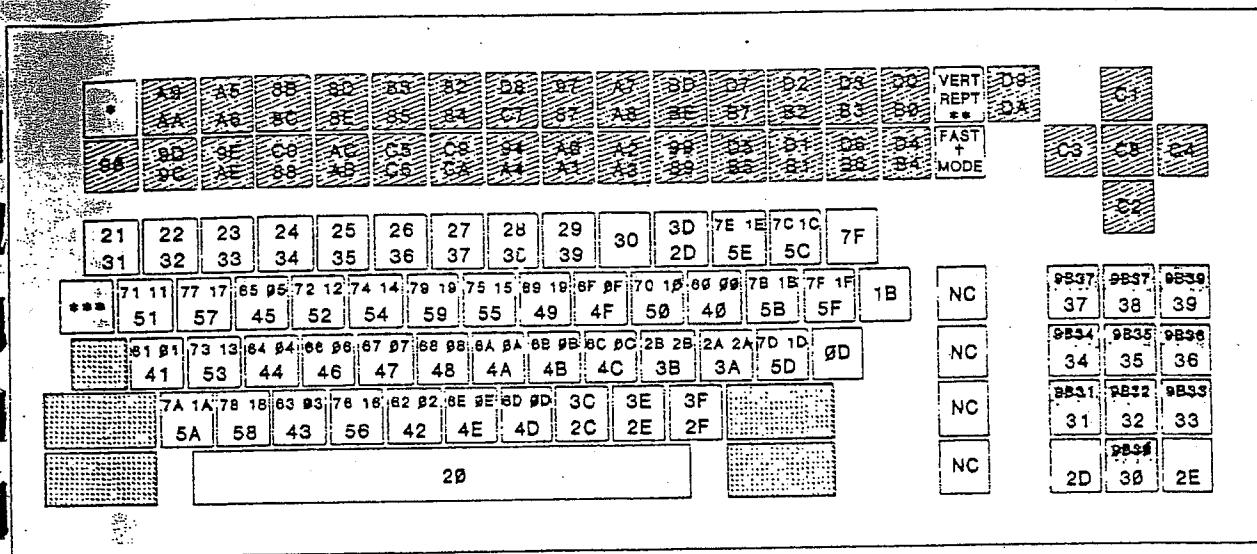
ON PROCESSOR BOARD

The first hex digit of the memory address is the Device Select digit.

Figure C-1. 5216 System Memory Map

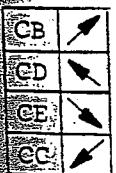
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## APPENDIX D



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- \* DC Power Indicator - No Switch
- \*\* produces a double character transmission: Character key code followed by (CC<sub>H</sub>)
- \*\*\* Causes repeat of previous character
- + Causes 500 characters/second rate
- NC No Code



These codes are generated by simultaneous actuation of two orthogonal cursor movement keys designating a quadrant.

Figure D-1. 5116 Keypad Codes

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The areas shaded [ ] (shift keys) will produce no serial output but may change the code of the character keyswitch depressed simultaneously.

#### Control Codes

The areas shaded [ ] are control codes and do not place a character on the screen but may cause a visual effect. The actual effect is determined by the 5216. The 5116 simply produces the codes shown in Figure D-1.

It should be noted that the four cursor keys in this field can be combined into pairs. The output code is a unique code to indicate diagonal cursor movement.

#### Function Codes

The areas shaded [ ] are the function code keys and will produce the codes shown in Figure D-1 when a shift and a number code keys are depressed. The function codes will not repeat.

40	41	42	43	44	45	46	47	48	49	50	51	52	53	54
25	26	27	28	29	30	31	32	33	34	35	36	37	38	39
10	11	12	13	14	15	16	17	18	19	20	21	22	23	24

Figure D-2. Decimal Numbering of Buttons on a 45 Function Keypad

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## APPENDIX E

2	3	4	5	6	7	8	9	10	11	12	13	14	15
DC POWER	REV CODE RPL	ADD 5x5	LUT#1 5x7	LUT#2 7x9	XKB PROG	XKB EIKOM	THOR/ LOAD	LKB WRIT G	TKB MCVE G	ALPHA KEYD GRAPHIC	LTC ERASE	ZOOM WORD	VERT RPT
CLEAR	BUFFER	LINE	CIRCLE	LOAD	LP	LOAD COLOR TABLE	LOAD SCROLL DOWN	PALETT	DEST	LTR	RBR	CRL	FAST MCDE
16	17	18	19	20	21	22	23	24	25	26	27	28	29

!	"	#	\$	%	&	'	(	)	0	=	~	Σ	;	↑
1	2	3	4	5	6	7	8	9	-	Λ	\	DELT		
REPEAT	q	W	e	g	r	τ	t	φ	Y	U	i	o	P	ESC
SHIFT LOCK	Q	W	E	R	F	G	H	J	A	K	L	;	;	-
Z	→	X	Ω	ε	g	V	ψ	b	Y	ν	o	m	£	RETURN
CONTROL SHIFT	Z	X	Ω	ε	g	V	ψ	b	Y	ν	o	M	,	?
SHIFT													/	CONTROL SHIFT

803-5194  
(sht. 2)

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Figure E-1. Function Code Keytop Engravings for Standard Firmware

COLOR VALUES	
00	Black
07	Red
38	Green
3F	Yellow
C0	Blue
C7	Magenta
F8	Cyan
FF	White

Figure E-2. Color Value Chart for the VID-004 Card

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## APPENDIX F

5116 FUNCTION CODES FOR  
STANDARD FIRMWARE AND ANCS  
(REFER TO FIGURE E-1)

ANCS Function Code (Hexadecimal)	Function Code (Hexadecimal)	Standard Firmware	Reverse Normal	ANCS Function
5216 Switch #16	Shift No Shift	A9 AA	Background Background	Reverse Video Normal Video
	Shift No Shift	A5 A6	Add Mode Replace Mode	Blink On Blink Off
3	Shift No Shift	8B 8C	LUT #1 5 X 7 Font	Relay On Relay Off
4	Shift No Shift	8D 8E	LUT #2 7 X 9 Font	Erase Page Clear Line
5	Shift No Shift	8E 85	Execute Keyboard Buffer 10 X 14 Font	Delete Line Insert Line
6	Shift No Shift	82 84	Execute Cache Buffer Programmable Font	Delete Character Insert Character
7	Shift No Shift	D8 C7	Memory Not Available	Not Available Split
8	Shift No Shift	97 87	Load Keyboard Buffer Write Graphic	Direct Addressing Relative to Memory Rotate
9	Shift No Shift	A7	Terminate Keyboard Buffer	Intensity Half
10	Shift No Shift	A8	Move Graphic	Intensity Full
11	Shift No Shift	BD BE	Attach Keyboard to ANCS Attach Keyboard to Graphics	Attach Keyboard to Graphics
12	Shift No Shift	D7 B7	Left Top Conic Erase Mode	Not Available White
13	Shift No Shift	D2 B2	Right Bottom Conic Cursor to Center	Not Available Green

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AYDIN 5116 Switch #	Shift / No Shift	Function Code (Hexadecimal)	5216 Standard Firmware	ANCS Function
14	Shift No Shift	D3 B3	Clear Left Conic Word Mode	Not Available Yellow
15	Shift No Shift	D0 B0	ZOOM Load Foreground Pixel	Not Available Black
16	Shift No Shift	9D 9C	Interrupt Interrupt	Xmit BUFFER Xmit SCREEN
17	Shift No Shift	9E AE	Interrupt Interrupt	Xmit Line Xmit Cursor
18	Shift No Shift	C0 88	Execute Circle Execute Vector	Built-in Test & Pattern Program Enter
19	Shift No Shift	AC AB	Not Available Load Index	Large Character Small Character
20	Shift No Shift	C5 C6	Scroll Up Scroll Down	Scroll Up Scroll Down
21	Shift No Shift	C9 CA	Not Available Load Color Table	Page Up Page Down
22	Shift No Shift	9A A4	Select Minor Channel Select Major Channel	Reset Attributes Format
23	Shift No Shift	A0 A1	Roll Left Roll Right	A/N Attribute Alpha
24	Shift No Shift	A2 A3	Pallet Special Font	Numeric Protect
25	Shift No Shift	99 89	Destuctive Non-Destructive	Activate Continuous Scroll Tab
26	Shift No Shift	D5 B5	Left Top Rectangle Clear to 1's	Not Available Magenta

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ATDUB 5116	Shift No Shift	Function Code (Hexadecimal)	5216 Standard Firmware	ANCS Function
27	Shift No Shift	D1 B1	Right Bottom Rectangle Copy	Not Available Red
28	Shift No Shift	D6 B6	Clear Rectangular Limits Pixel Mode	Not Available Cyan
29	Shift No Shift	D4 B4	Fill Load Background Pixel	Not Available Blue

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## APPENDIX G

### OPERATION CODE SUMMARY

This appendix should have the commands listed (with diagrams) in the following order: Those w/out page numbers will have to be completed when tex is prepared.

START OF MESSAGE (SOM) pg. 2-1  
LOAD RECTANGULAR LIMITS (LRR, LRL, LRT, LRB) pg. 3-1  
MODE CONTROL WORD (MCW) pg. 4-1  
ADJUSTABLE CURSOR ADVANCE (ACA) pg. 5-2  
CURSOR POSITIONING (LCX, LCY, CUR) pg. 5-3  
VISIBLE CURSOR CONTROL (VCC) pg. 5-5  
LOAD INDEX REGISTERS (LIX, LIY) pg. 5-5  
LOAD ALPHANUMERIC CHARACTER (LAC) pg. 6-2  
LOAD PROGRAMMABLE FONT (LPF) pg. 6-3  
WRITE PIXEL VALUE (WPX, WPXF, WPXB) pg. 7-1, 7-2  
LOAD GRAPHIC ELEMENTS (LGE) pg. 7-2  
EXECUTE CONIC INSTRUCTION (EXC) pg. 7-3  
LOAD CONIC LIMITS (LCLL, LCLR, LCLT, LCLB) pg. 7-4  
BLOCK TRANSFER MODE (BXM) pg. 8-1  
EXTENDED BLOCK TRANSFER MODE (XBXM) pg. 8-4  
EDIT INSTRUCTION (EDT) pg. 9-1  
EXTENDED EDIT (XEDT) pg. 9-3  
ZOOM pg. 9-3  
FILL pg. 9-3  
COPY pg. 9-4  
LOAD PIXEL REGISTERS (LRXF, LPXB) pg. 10-2  
SELECT MAJOR CHANNEL (SMC) pg. 11-2  
SELECT MINOR CHANNELS (SMN) pg. 11-2  
MASK MAJOR CHANNEL (MMC) pg. 11-3  
MASK MINOR CHANNELS (MMN) pg. 11-3  
EXTENDED SELECT MAJOR CHANNEL (XSMC) pg. 11-4  
EXTENDED MASK MAJOR CHANNEL (XMMC) pg. 11-4  
LOAD LOOKUP TABLE 004 (LLU4) pg. 12-3  
EXTENDED LOAD LOOKUP TABLE (XLLU4) pg. 12-4

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READ LOOKUP TABLE 004 (RLU4) pg. 12-4  
EXTENDED READ LOOKUP TABLE 004 (XRLU4) pg. 12-5  
CURSOR POSITION READBACK (TCO) pg. 13-1  
EXTENDED CURSOR POSITION READBACK (XTCO) pg. 13-2  
MEMORY DATA READBACK (TSC) pg. 13-3  
EXTENDED MEMORY DATA READBACK (XTSC) pg. 13-3  
TRANSMIT PICTURE ELEMENTS (TPX) pg. 13-3  
KEYBOARD TRANSMIT STATUS (KTS) pg. 13-4  
END OF TRANSMISSION (EOT) pg. 13-6  
LOAD CACHE BUFFER (LCB) pg. 14-1  
READ CACHE BUFFER (RCB) pg. 14-2  
EXECUTE CACHE BUFFER (XCB) pg. 14-3  
EXECUTE KEYBOARD BUFFER (XKB) pg. 14-4  
STANDARD LIST INSTRUCTIONS pgs. 17-5 through 17-8

SELECT DEVICE (SDEV) pg. 15-1  
ERROR CODE (ERC) pg. 16-1/16-2

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## OPERATION CODE SUMMARY

Start of Message - SOM (page 2-1)

SOM	Parameter Set															
	1	1	1	0	1	1	0	0	-	-	-	-	-	PS <sub>4</sub>	PS <sub>3</sub>	PS <sub>2</sub>
15									8	7		4	3			0

Parameters

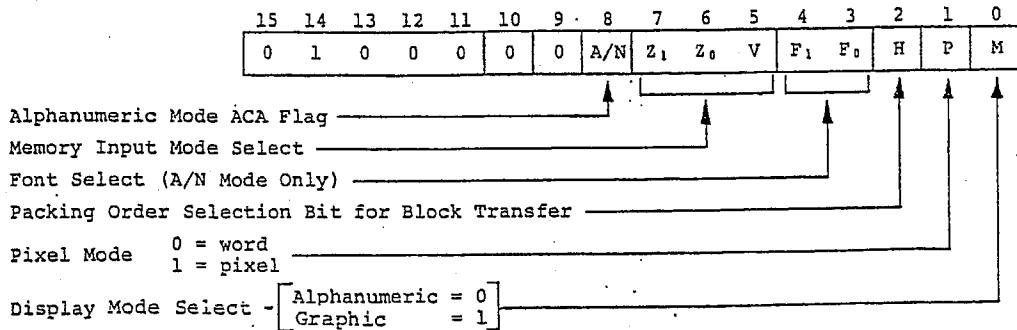
- MAJOR Channel Selects
- MINOR Channel Select
- MAJOR Channel Mask
- MINOR Channel Mask
- Mode Control Word
- X - cursor position
- Y - cursor position
- X - index
- Y - index
- Foreground pixel value
- Background pixel value
- Rectangular limits
- Conic limits
- ACA values
- KTS

Load Rectangular Limits - LRR, LRL, LRT, LRB (page 3-1)

	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
LEFT - LRL	0	1	0	1	0	0	L	L	L	L	L	L	L	L	L	L
RIGHT - LRR	0	1	0	1	1	0	R	R	R	R	R	R	R	R	R	R
TOP - LRT	0	1	1	0	0	0	T	T	T	T	T	T	T	T	T	T
BOTTOM - LRB	0	1	1	0	1	0	B	B	B	B	B	B	B	B	B	B
																BINARY VALUE OF LIMIT

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## Mode Control Word - MCW (page 4-1)



Packing Order 0 = low then high  
1 = high then low

ACA Flag 0 = Font default  
1 = use ACA values

## Standard Font Cursor Advance (page 4-3)

Cursor Advance in Selected Font (number of picture elements)					
Bit No.	4	3	Font Selected	X	Y
	0	0	5 x 7	8	10
	0	1	7 x 9	10	14
	1	0	10 x 14	16	16
	1	1	16 x 15 (Programmable Font)	x-cur.adv.	y-pitch

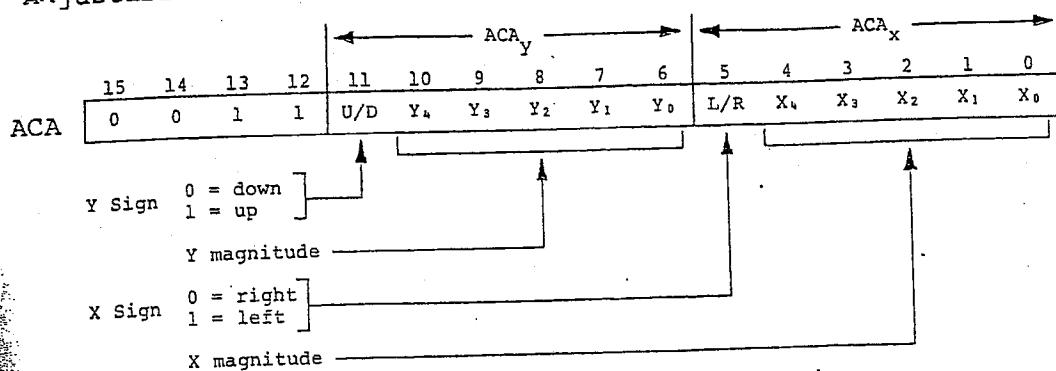
Bits 3 and 4 have no effect in the graphic mode.  
The X and Y cursor advances shown are used unless bit 8 of MCW is set.

## Memory Input Mode Selection (page 4-3)

MCW Bit No.	7	6	5	Memory Input Mode
Designation	Z <sub>1</sub>	Z <sub>0</sub>	V	
	0	0	0	OR Ones
	0	1	0	Replace Normal
	0	1	1	Replace Reverse
	1	0	0	Erase Ones
	1	1	X	Not Defined

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## Adjustable Cursor Advance (ACA) pg. 5-2



## Cursor Positioning - LCX, LCY, CUR (page 5-3)

LCX

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	1	1	0	R	A <sub>X</sub>									

LCY

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	1	1	1	R	A <sub>Y</sub>									

Relative Flag → Cursor X, Y  
Coordinate Value

R = 0 Cursor position is specified absolutely

R = 1 Cursor position is specified as an increment  
relative to the current cursor position

CUR

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	1	1	0	1	0	-	-	W <sub>d</sub>	Y <sub>0</sub>	X <sub>0</sub>	+	+	+	+

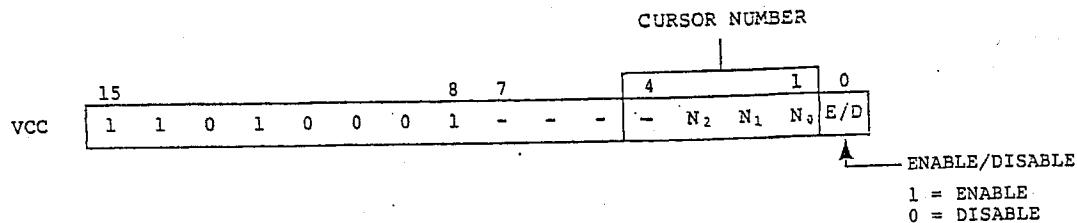
Write Dot →  
Y Home →  
X Home →

Relative Movement →  
depends on Display  
Mode and A/N bit of MCW

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Visible Cursor Control - VCC (page 5-5)

\*EXT

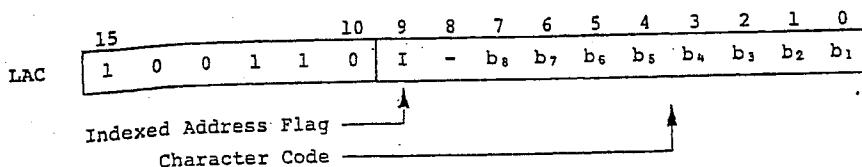


Load Index Registers - LIX, LIY (page 5-5)

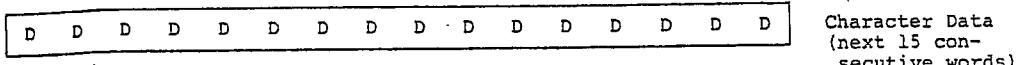
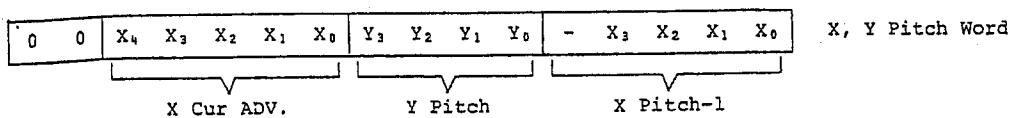
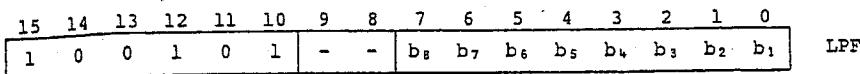
LIX	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
	1	0	0	0	0	0	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>
LIY	1	0	0	0	0	1	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>

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## Load Alphanumeric Character - LAC (page 6-2)



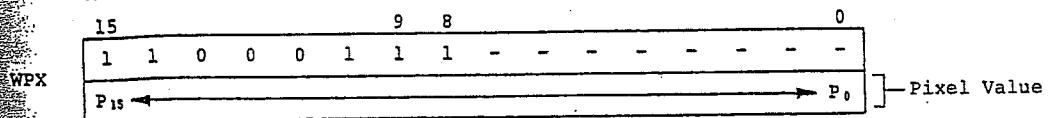
## Load Programmable Font - LPF (page 6-3)



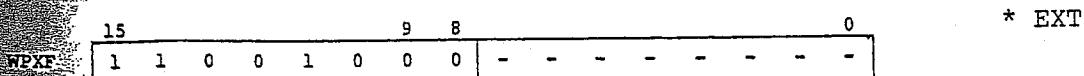
## Write Pixel Values - WPX, WPXF, WPXB (pages 7-1, 7-2)

\* EXT

## Write Pixel



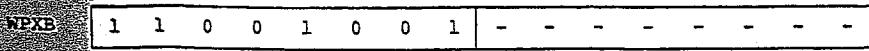
## Write Pixel - Foreground



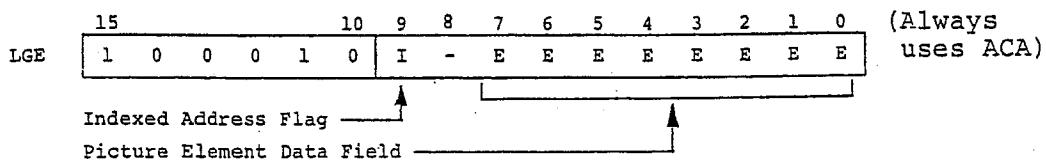
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## Write Pixel - Background

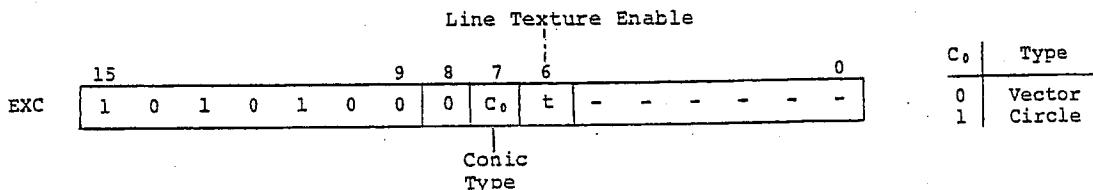
\* EXT



## Load Graphic Elements - LGE (page 7-2)



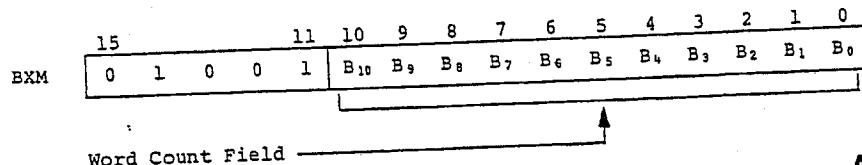
## Execute Conic - EXC (page 7-3)



## Load Conic Limits - LCLL, LCLR, LCLT, LCLB (page 7-4)

LEFT - LCLL	15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0	0 1 0 1 0 1   L L L L L L L L L L L
RIGHT - LCLR		0 1 0 1 1 1   R R R R R R R R R R R
TOP - LCLT		0 1 1 0 0 1   T T T T T T T T T T T
BOTTOM - LCLB		0 1 1 0 1 1   B B B B B B B B B B

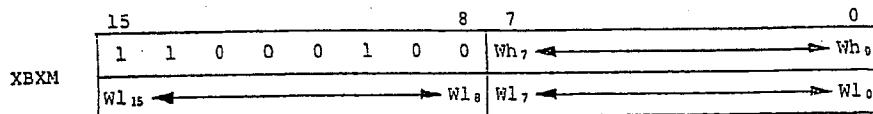
## Block Transfer Mode - BXM (page 8-1)



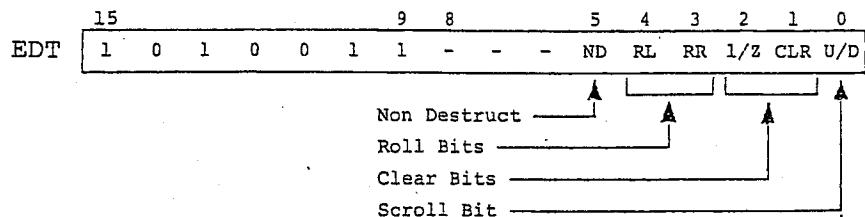
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## Extended Block Transfer - XBXM (page 8-4)

\* EXT



## Scrolling, Rolling, Clearing - Edit Instruction EDT (page 9-1)



ND = 1 is non-destructive  
 ND = 0 is destructive

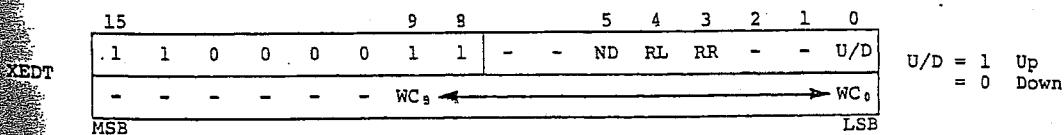
U/D = 1 Up  
 = 0 Down

Bit No.	2	1	0	Meaning
	1/Z	CLR	SCR	
0	0	0	0	Scroll Down
0	0	1	0	Scroll Up
0	1	0	0	Clear to Zeros
1	1	0	0	Clear to Ones

Bit	4	3	Meaning
0	1	0	Roll Right
1	0	0	Roll Left
1	1	0	Danger

## Extended Edit - XEDT (page 9-3)

\*EXT



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**Zoom, Fill, Copy (pages 9-3, 9-4)**

TYPE CODE																								
15								8	7	2		1	0	* EXT										
COPY								1	1	0	1	0	1	0	0	-	-	-	-	-	T <sub>1</sub>	T <sub>0</sub>	Pixel Value #1	
D <sub>15</sub>															D <sub>0</sub>		Pixel Value #2							
D <sub>15</sub>															D <sub>0</sub>		(optional)							

T <sub>1</sub>	T <sub>0</sub>	Number of Parameter Words	Will Copy
0	0	1	non-zero after AND
0	1	1	equals
1	0	2	in range
1	1	2	not in range

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### Load Pixel Registers (page 10)

Load Pixel Value - Foreground \*EXT

	15	9	8	0
LPXF	1 1 0 0 0 1 0 1 - - - - - - - -			
PIXEL VALUE	P <sub>15</sub> -			P <sub>0</sub>

Load Pixel Value - Background

\*EXT

LPXB	15	9	8	0
	1 1 0 0 0 1 1 0 - - - - - - - -			
PIXEL VALUE	P <sub>15</sub>			P <sub>0</sub>

Select Major Channel - SMC (page 11-2)

SMC	15	12	11	0	Bit No.
	0 0 0 1	c c c c c c c c c c c c c c			
		12 11 10 9 8 7 6 5 4 3 2 1			Channel number selected by corresponding bit number

Select Minor Channel - SMN (page 11-2)

SMN	15	9	8	4	3	2	1	0	
	1 0 1 0 0 0 0	- - - - -	S <sub>4</sub> S <sub>3</sub> S <sub>2</sub> S <sub>1</sub>						Refers to hardware optional configuration
		12 11 10 9 8 7 6 5 4 3 2 1							Minor Channel Select Field

Mask Major Channel - MMC (page 11-3)

MMC	15	12	11	0	
	0 0 1 0	c c c c c c c c c c c c c c			Error generated by masking selected or selecting masked channel
		12 11 10 9 8 7 6 5 4 3 2 1			Channel number masked by corresponding bit number

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Mask Minor Channel - MMN (page 11-3)

	15		9	8	4	3	2	1	0				
MMN	1	0	1	0	0	0	1	-	-	$S_4$	$S_3$	$S_2$	$S_1$

Extended Select Major Channel - XSMC (page 11-4) \*EXT

XSMC	15	.	9	8	0											
	1	1	0	0	0	-										
	c	c	c	c	c	c										
	16	15	14	13	12	11	10	.9	8	7	6	5	4	3	2	1

Extended Mask Major Channel - XMMC (page 11-4) \*EXT

XMMC	15	9	8	0	
	1	1	0	0	- - - - - - - -
	c	c	c	c	c c c c c c c c
	16	15	14	13	12 11 10 9 8 7 6 5 4 3 2 1

Load Lookup Table 004 - LLU4 (page 12-3) \*EXT

LLU4 AX209951

Color Word Enable								Status Enable							
15	9	8	7	6	5	2	1	0	STATUS WORD		TBL #				
1	0	1	0	1	1	0	-	CE	SE	S <sub>4</sub>	S <sub>3</sub>	S <sub>2</sub>	S <sub>1</sub>	T <sub>1</sub>	T <sub>0</sub>
A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	B <sub>2</sub>	B <sub>1</sub>	G <sub>3</sub>	G <sub>2</sub>	G <sub>1</sub>	R <sub>3</sub>	R <sub>2</sub>	R <sub>1</sub>
TABLE ADDRESS								Optional depending on CW CE = 1 expects second word							

Extended Load Lookup Table 004 - XLLU4 (page 12-4)

\*EXT

STATUS ENABLE

XLLU4								STATUS ENABLE		TABLE #		
1 1 0 0 1 0 1 1								1   S <sub>4</sub> S <sub>3</sub> S <sub>2</sub> S <sub>1</sub>		T <sub>3</sub> T <sub>2</sub> T <sub>1</sub> T <sub>0</sub>		
ADDRESS								COUNT-1				
A <sub>8</sub> A <sub>7</sub> A <sub>6</sub> A <sub>5</sub> A <sub>4</sub> A <sub>3</sub> A <sub>2</sub> A <sub>1</sub>								C <sub>7</sub> C <sub>6</sub> C <sub>5</sub> C <sub>4</sub> C <sub>3</sub> C <sub>2</sub> C <sub>1</sub> C <sub>0</sub>				
NOT USED								DATA WORD 1				
								0 0 0 0				
NOT USED								DATA WORD N				

Read Lookup Table 004 - RLU4 (page 12-4)

\*EXT

Color Word Enable      Status Word Enable

RLU4								9	8	7	6	5	2	1	0
15	1	0	1	0	1	1	-	CE	SE	X	X	X	X	TBL #	
A <sub>8</sub> A <sub>7</sub> A <sub>6</sub> A <sub>5</sub> A <sub>4</sub> A <sub>3</sub> A <sub>2</sub> A <sub>1</sub>								-	-	-	-	-	-	T <sub>2</sub>	T <sub>1</sub>

Extended Read Lookup Table 004 - XRLU4 (page 12-5)

\*EXT

STATUS ENABLE

XRLU4								7	6	5	1	0
15	1	1	0	0	1	1	0	0	SE	S <sub>4</sub> S <sub>3</sub> S <sub>2</sub> S <sub>1</sub>	T <sub>3</sub> T <sub>2</sub> T <sub>1</sub>	
WORD 1								Word Count - 1 *EXT				
WORD 2								C <sub>7</sub> . . . . . C <sub>0</sub>				

Cursor Position Readback - TCO (page 13-1)

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15      10    9      4    3      0

TCO	1	0	0	0	1	1	-	-	-	-	-	-	PS <sub>3</sub> PS <sub>2</sub> PS <sub>1</sub> PS <sub>0</sub>
-----	---	---	---	---	---	---	---	---	---	---	---	---	---

Extended Cursor Position Readback - XTCO (page 13-2)

\*EXT

15 4 3 0  
XTCO 1 1 0 0 1 1 0 1 - - - PS<sub>3</sub> PS<sub>2</sub> PS<sub>1</sub> PS<sub>0</sub>

Memory Data Readback - TSC (page 13-3)

15 9 0  
TSC 1 0 1 1 0 1 1 - - - - - - - -

Extended Memory Data Readback - XTSC (page 13-3)

\*EXT

Uses BXM Word mode  
one channel at a time.

15 8 0  
XTSC 1 1 0 0 1 1 1 0 - - - - - - - -

Transmit Picture Elements - TPX (page 13-3)

\*EXT

15 9 8 0  
TPX 1 1 0 0 1 0 1 0 - - - - - - - -

Uses BXM Pixel Mode

Keyboard Transmit Status - KTS (page 13-4)

AX209953

Function Code Disable (to DG) 9 ↓ Parameter Set Number  
15 5 2 1 0  
KTS 1 0 1 1 0 0 0 CD<sub>2</sub> CD<sub>1</sub> CD N<sub>3</sub> N<sub>2</sub> N<sub>1</sub> N<sub>0</sub> TK<sub>1</sub> TK<sub>0</sub>  
Cursor Disable (to Host) To Computer To Display Generator  
Keystroke Disable (to DG)

End of Transmission - EOT (page 13-6)

	15	9	8	0
EOT	1	1	1	- - - - -

Load Cache Buffer - LCB (page 14-1)

\*EXT

LCB	15	9	8	0	
	1	1	1	0	0 1 0 - - - - -
	$\lambda_{15}$				$\lambda_0$
	- - - - -			$DS_3$	$DS_0$
	$WC_{15}$			$WC_0$	$WC_0$
	DATA				
	End of Field Code				

End of Field Code  
= FC00 hex for 5216  
= CB hex for 8086

Read Cache Buffer - RCB (page 14-2)

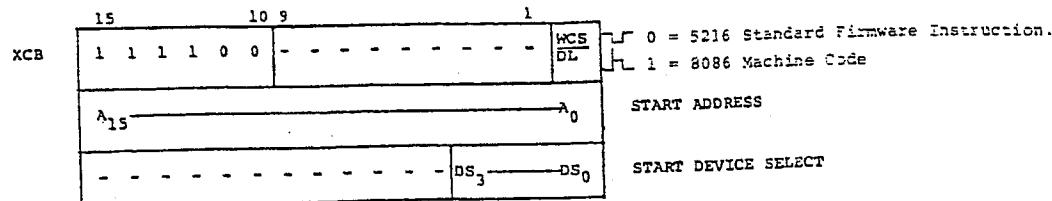
\*EXT

RCB	15	8	7	0	
	1	1	1	0	1 0 0 0 0 - - - - -
	$\lambda_{15}$				$\lambda_0$
	- - - - -			$DS_3$	$DS_0$
	$WC_{15}$			$WC_0$	$WC_0$

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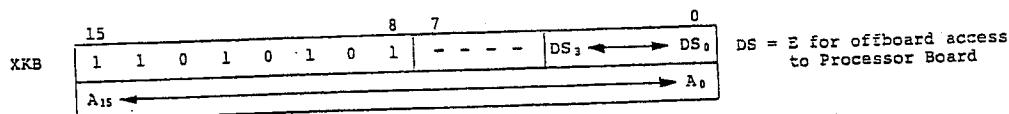
Execute Cache Buffer - XCB (page 14-3)

\*EXT



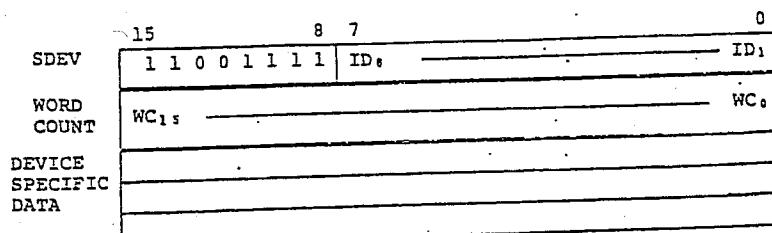
Execute Keyboard Buffer - XKB (page 14-4)

\*EXT



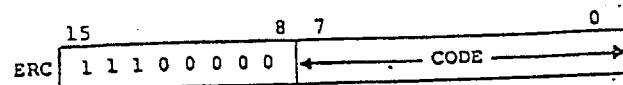
Select Device - SDEV (page 15-1)

\*EXT



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Error Code - ERC (page 16-1)



Code

1	Host/5216 Communications Error
3	Cursor out of limits
8	Illegal (syntax) instruction

**AX209956**

Standard List Instructions (pages 14-5 though 14-8)

SETCTR

15	10	9	8	7	0
1	0	1	1	1	0

COUNTER NUMBER = 1, 2, or 3

INCCTR

15	8	7	3	2	1	0
1	0	1	1	1	0	0

COUNTER NUMBER =  
0 = INCREMENT  
1 = DECREMENT

LOOP

15	9	8	7	0
1	0	1	1	1

COUNTER NUMBER = 1, 2, or 3

JMPZ

15	10	9	8	7	0
1	1	1	0	0	0

COUNTER NUMBER = 1, 2, or 3

JMPPOS

15	10	9	8	7	0
1	1	0	1	1	1

COUNTER NUMBER = 1, 2, or 3

JMPNEG

15	10	9	8	7	0
1	1	0	1	1	0

COUNTER NUMBER = 1, 2, or 3

JMPL

15	8	7	0
1	1	0	1

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PSHLST

15	8	7	2	1	0
1	1	0	0	0	1

COUNTER NUMBER = 1, 2, or 3

POPLST

15	8	7	2	1	0
1	1	0	1	0	0

COUNTER NUMBER = 1, 2, or 3

ALIST

15	8	7	4	3	0
1	1	0	1	0	1
1 1 0 1 0 1 1 1					DSEL

OFFSET ADDRESS

AX209958

## Instruction Set Summary

	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0				
NO OPERATION (NULL)	0	0	0	0	-	-	-	-	-	-	-	-	-	-	-	-				
SELECT MAJOR CHANNEL	0	0	0	1	C <sub>12</sub>	C <sub>11</sub>	C <sub>10</sub>	C <sub>9</sub>	C <sub>8</sub>	C <sub>7</sub>	C <sub>6</sub>	C <sub>5</sub>	C <sub>4</sub>	C <sub>3</sub>	C <sub>2</sub>	C <sub>1</sub>				
MASK MAJOR CHANNEL	0	0	1	0	C <sub>12</sub>	C <sub>11</sub>	C <sub>10</sub>	C <sub>9</sub>	C <sub>8</sub>	C <sub>7</sub>	C <sub>6</sub>	C <sub>5</sub>	C <sub>4</sub>	C <sub>3</sub>	C <sub>2</sub>	C <sub>1</sub>				
ADJUSTABLE CURSOR ADVANCE	0	0	1	1	U/D	Y <sub>4</sub>	Y <sub>3</sub>	Y <sub>2</sub>	Y <sub>1</sub>	Y <sub>0</sub>	L/R	X <sub>4</sub>	X <sub>3</sub>	X <sub>2</sub>	X <sub>1</sub>	X <sub>0</sub>				
MODE CONTROL WORD	0	1	0	0	0	0	1	0	-	A/N	Z <sub>1</sub>	Z <sub>0</sub>	V	F <sub>1</sub>	F <sub>0</sub>	H				
BLOCK TRANSFER MODE	BXM	0	1	0	0	1	0	1	B <sub>10</sub>	B <sub>9</sub>	B <sub>8</sub>	B <sub>7</sub>	B <sub>6</sub>	B <sub>5</sub>	B <sub>4</sub>	B <sub>3</sub>	B <sub>2</sub>	B <sub>1</sub>	B <sub>0</sub>	
LOAD RECTANGULAR LIMIT LEFT	LRL	0	1	0	1	0	1	0	CL	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	
LOAD RECTANGULAR LIMIT RIGHT	LRR	0	1	0	1	0	1	0	CL	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	
LOAD RECTANGULAR LIMIT TOP	LRT	0	1	1	0	1	0	1	CL	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	
LOAD RECTANGULAR LIMIT BOTTOM	LRB	0	1	1	0	1	0	1	CL	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	
LOAD CURSOR X	LCX	0	1	1	1	0	1	0	R	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	
LOAD CURSOR Y	LCY	0	1	0	0	0	0	1	0	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	
LOAD INDEX X REG.	LIX	1	0	0	0	0	0	1	0	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	
LOAD INDEX Y REG.	LYI	1	0	0	0	0	0	1	0	A <sub>9</sub>	A <sub>8</sub>	A <sub>7</sub>	A <sub>6</sub>	A <sub>5</sub>	A <sub>4</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	
LOAD GRAPHIC ELEMENTS	LGE	1	0	0	0	0	0	1	0	-	-	-	-	-	-	-	-	-	-	
TRANSMIT CURSOR ONLY	TCO	1	0	0	0	0	0	1	0	-	-	-	-	-	-	-	-	-	-	
LOAD PROGRAMMABLE FONT	LPF	1	0	0	0	1	0	1	0	-	-	-	-	-	-	-	-	-	-	
LOAD ALPHANUMERIC CHAR.	LAC	1	0	0	1	0	1	0	0	-	-	-	-	-	-	-	-	-	-	
SELECT MINOR CHANNEL	SMN	1	0	0	1	0	0	0	0	-	-	-	-	-	-	-	-	-	-	
MASK MINOR CHANNEL	MMN	1	0	1	0	0	0	1	0	-	-	-	-	-	-	-	-	-	-	
EDIT	EDT	0	1	0	0	1	0	0	1	0	0	0	0	C <sub>6</sub>	C <sub>5</sub>	C <sub>4</sub>	C <sub>3</sub>	C <sub>2</sub>	C <sub>1</sub>	C <sub>0</sub>
EXECUTE CONIC	EXC	1	0	1	0	1	0	0	0	CD <sub>2</sub>	CD <sub>1</sub>	CD	N <sub>3</sub>	N <sub>2</sub>	N <sub>1</sub>	N <sub>0</sub>	TK <sub>1</sub>	TK <sub>0</sub>	-	
KEYBOARD TRANSMIT STATUS	KTS	1	0	1	0	1	0	0	1	0	-	W <sub>0</sub>	Y	X	4	+	+	+	-	
CURSOR MOVEMENT	CUR	1	0	1	0	1	0	1	0	-	-	-	-	-	-	-	-	-	-	
TRANSMIT SELECTED CHANNEL	TSC	1	0	1	1	0	1	0	1	-	-	-	-	-	-	-	-	-	-	
END TRANSMISSION CODE	ETC	1	1	1	0	1	1	0	1	-	-	-	-	-	-	-	-	-	-	
START OF MESSAGE CODE	SOM	1	1	1	1	0	1	1	0	-	-	-	-	-	-	-	-	-	-	

- Indicates "don't care" bit

AX209959

## APPENDIX H

## EXTENDED INSTRUCTION SET SUMMARY

## Extended Instruction Set (cont)

AX209960

Extended Instruction Set

Extended Select Major Channel	XSMC	15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
Channel Bit Map	Data	1 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0
Extended Mask Major Channel	XMMC	1 1 0 0 0 0 1 - - - - - - - - -
Channel Bit Map	Data	C <sub>15</sub>  C <sub>0</sub> 
Load Look Up Table - 4 Address and Color Value	LLU4	1 0 1 0 1 1 0 - CW SE S <sub>4</sub> S <sub>3</sub> S <sub>2</sub> S <sub>1</sub> T <sub>1</sub> T <sub>0</sub>
Read Look Up Table - 4 Table Address	RLU4	1 0 1 0 1 1 - CW SE - - - - T <sub>1</sub> T <sub>0</sub>
Address and Word Count	Data	A <sub>6</sub>  A <sub>1</sub> - - - - - - -
N Words Color Value	YLLU4	1 1 0 0 1 1 1 S <sub>4</sub> S <sub>3</sub> S <sub>2</sub> S <sub>1</sub> T <sub>3</sub> T <sub>2</sub> T <sub>1</sub> T <sub>0</sub>
Extended Load Look Up Table - 4 Address and Word Count	Data	A <sub>6</sub>  A <sub>1</sub> N <sub>7</sub>  N <sub>0</sub> 
N Words Color Value	Data	- - - - - C <sub>7</sub>  C <sub>0</sub> 
Extended Read Look Up Table - 4 Address and Word Count	YRLU4	1 1 0 0 1 1 0 0 1 S <sub>4</sub> S <sub>3</sub> S <sub>2</sub> S <sub>1</sub> T <sub>3</sub> T <sub>2</sub> T <sub>1</sub> T <sub>0</sub>
Extended Cursor Position Readback	XTCO	1 1 0 0 1 1 0 1 - - - - PS <sub>3</sub> PS <sub>2</sub> PS <sub>1</sub> PS <sub>0</sub>
Extended Memory Data Readback	XMSC	1 1 0 0 1 0 1 0 - - - - - - - -
Transmit Picture Elements	TPX	1 1 0 0 1 0 1 0 - - - - - - - -

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## Extended Instruction Set (cont.)

	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Load Cache Buffer	LCB	1	1	0	0	1	0	-	-	-	-	-	-	-	-	-
Address	Data	A <sub>15</sub>	→													→ A <sub>0</sub>
Data	Data	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-
Device Select	Data	WC <sub>15</sub>	→													→ WC <sub>0</sub>
Word Count	Data	D <sub>15</sub>	→													→ D <sub>0</sub>
N-1 words Data	Data															
End of Field	Data															
FC00 hex or CB hex																
Read Cache Buffer	RCB	1	1	0	1	0	0	0	-	-	-	-	-	-	-	-
Start Address	Data	A <sub>15</sub>	→													→ A <sub>0</sub>
Device Select	Data	-	-	-	-	-	-	-	-	-	-	-	-	-	-	
Word Count	Data	WC <sub>15</sub>	→													→ WC <sub>0</sub>
Execute Cache Buffer	XCB	1	1	1	1	0	0	-	-	-	-	-	-	-	-	W/D
Start Address	Data	A <sub>15</sub>	→													→ A <sub>0</sub>
Device Select	Data	-	-	-	-	-	-	-	-	-	-	-	-	-	-	
Execute Keyboard Buffer	XKB	1	1	0	1	0	1	-	-	-	-	-	-	-	-	
Start Address	Data	A <sub>15</sub>	→													
Select Device	SDEV	1	1	0	0	1	1	1	1	ID <sub>0</sub>	→					→ ID <sub>1</sub>
Word Count	Data	WC <sub>15</sub>	→													→ WC <sub>0</sub>
Device Specific Data-N Words	Data	D <sub>15</sub>	→													→ D <sub>0</sub>

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APPENDIX I  
INSTRUCTION SET IN NUMERICAL ORDER

<u>HEX</u>	<u>OCTAL</u>	<u>MNEMONIC</u>	<u>NAME</u>
0XXX	00XXXX	NOP	NO-OP
1XXX	01XXXX	SMC	Select Major Channel
2XXX	02XXXX	MMC	Mask Major Channel
3XXX	03XXXX	ACA	Adjustable Cursor Advance
40XX	040XXX	MCW	Mode Control Word
48XX	044XXX	BXM	Block Transfer Mode
↓	↓		
4FXX	047XXX		
50XX	050XXX	LRL	Load Rectangular Limit Left
↓	↓		
53XX	051XXX		
54XX	052XXX	LCLL	Load Conic Limit Left
↓	↓		
57XX	053XXX		
58XX	054XXX	LRR	Load Rectangular Limit Right
↓	↓		
5BXX	055XXX		
5CXX	056XXX	LCLR	Load Conic Limit Right
↓	↓		
5FXX	057XXX		
60XX	060XXX	LRT	Load Rectangular Limit Top
↓	↓		
63XX	061XXX		
64XX	062XXX	LCLT	Load Conic Limit Top
↓	↓		
67XX	063XXX		
68XX	064XXX	LRB	Load Rectangular Limit Bottom
↓	↓		
6BXX	065XXX		

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<u>HEX</u>	<u>OCTAL</u>	<u>MNEMONIC</u>	<u>NAME</u>
6CXX	066XXX	LCLB	Load Conic Limit Bottom
↓	↓		
6FXX	067XXX		
70XX	070XXX	LCX	Load Cursor X (absolute)
↓	↓		
73XX	071XXX		
↓	↓		
74XX	072XXX	LCX	Load Cursor X (relative)
77XX	073XXX		
78XX	074XXX	LCY	Load Cursor Y (absolute)
↓	↓		
7BXX	075XXX		
7CXX	076XXX	LCY	Load Cursor Y (relative)
↓	↓		
7FXX	077XXX		
80XX	100XXX	LIX	Load Index X Register
↓	↓		
83XX	101XXX		
84XX	102XXX	LIY	Load Index Y Register
↓	↓		
87XX	103XXX		
88XX	104XXX	LGE	Load Graphic Elements
↓	↓		
8BXX	105XXX		
8CXX	106XXX	TCO	Cursor Position Readback
94XX	112 XXX	LPF	Load Programmable Font
98XX	114XXX	LAC	Load Alphanumeric Character
↓	↓		
9AXX	115XXX		
A0XX	120XXX	SMN	Select Minor Channel
↓	↓		
A1XX			
A2XX	121XXX	MMN	Mask Minor Channels
↓			
A3XX			
A4XX	122XXX		
↓	↓		

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<u>HEX</u>	<u>OCTAL</u>	<u>MNEMONIC</u>	<u>NAME</u>
A5XX	123XXX	EDT	Edit (Clear/Scroll)
A6XX	124XXX	EXC	Execute Conic
A7XX	125XXX		
A8XX	126XXX	LLU4	Load Lookup Table 4
A9XX	127XXX	RLU4	Read Lookup Table 4
AAXX	130XXX	KTS	Keyboard Transmit Status
B4XX	132XXX	CUR	Cursor Movement
B5XX	133XXX	TSC	Memory Data Readback
B6XX	140XXX	XSMC	Extended Select Major Channel
C1XX	1404XX	XMMC	Extended Mask Major Channel
C3XX	1414XX	XEDT	Extended Edit
C4XX	142XXX	XBXM	Extended Block Transfer Mode
C6XX	1424XX	LPXF	Load Foreground Pixel Value
C6XX	1430XX	LPXB	Load Background Pixel Value
C7XX	1434XX	WPX	Write Pixel Value
C8XX	1440XX	WPXF	Write Foreground Pixel Value
C9XX	1444XX	WPXB	Write Background Pixel Value
CAXX	1450XX	TPX	Transmit Picture Elements
CBXX	1454XX	XLLU4	Extended Load Lookup Table 4
CCXX	146XXX	XRLU4	Extended Read Lookup Table 4
CDXX	1464XXX	XTCO	Extended Cursor Position Readback
CEXX	1470XX	XTSC	Extended Memory Data Readback
CFXX	1474XX	SDEV	Select Device
DIXX	1504XX	VCC	Visible Cursor Control
D4XX	1520XX	COPY	Copy
D5XX	1524XX	XKB	Execute Keyboard Buffer
E4XX	162XXX	LCB	Load Cache Buffer
E6XX	1630XX	ZOOM	Zoom
E7XX	1634XX	FILL	Fill

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<u>HEX</u>	<u>OCTAL</u>	<u>MNEMONIC</u>	<u>NAME</u>
E8XX	1640XX	RCB	Read Cache Buffer
ECXX	166XXX	SOM	Start of Message
FXXX	170XXX	XCB	Execute Cache Buffer
FCXX	176XXX	EOT	End of Transmission

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